

LAW DEPARTMENT FREESCALE SEMICONDUCTOR, INC.

DATE:

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Docket No.: SC13166TC

Applicant: Ahmad H. Atriss et al.

Serial No.: 10/804,453

Art Unit:

2819

Filed:

March 19, 2004

Class:

341-172000

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

1.	x	1 page Facsimile Cover Sheet
2	X	1 page PTOL-85B Issue Fee Transmittal (in duplicate)
3.	×	3 page Comments on Statement of Reasons for Allowance
Paid by Deposit Account 503079, Freescale Semiconductor, Inc: \$1700		

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BOCKET NO. SCI3166TC

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT

Ahmad H. Atriss et al.

GROUP ART UNIT: 2819

APPLN. NO.:

10/804,453

EXAMINER:

Khai M. Nguyen

FILED:

March 19, 2004

CONFIRMATION No.: 7270

TITLE:

OPTIMIZED REFERENCE VOLTAGE GENERATION USING

SWITCHED CAPACITOR SCALING FOR DATA

CONVERTERS

Certificate of Transmission under 37 C.F.R 1.8

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This paper is filed contemporaneous with the filing of the issue fee for this application. Entrance of the comments provided below regarding the statement of reasons for allowance provided by the Examiner is respectfully requested.

FEES

Applicants authorize the charging of Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any fees owed in connection with this application, or credit Deposit Account No. 503079 (Freescale Semiconductor, Inc.) for any refunds.

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REMARKS

In a communication mailed April 19, 2005, claims 1, 3-5, 7-23, 25, 27-28, 30-44 and 46-47 were allowed. The Examiner provided a statement of reasons for allowance. Applicants respectfully submit that the recited combination of features recited within claims 23, 25, 27-42 and 43 are not taught or suggested by the prior art made of record. Additionally, claim 44 was stated to be allowable because of the recited subject matter and a direct quotation of a portion of the claim 44 was made. The combination of all of the recitals of claim 44 contributes to the claim's novel. It should also be understood that Applicants defined the phrase "a range between two power supply potentials" to mean a range that includes the two power supply potentials and all intervening values from each power supply potential. In other words, the range starts at one of the two power supply potentials and extends to the other power supply potential inclusively. Therefore, voltages of the second recited operating range may include the two power supply potential values as being "substantially within the first operating range" as recited in claim 44. Further, the recited scaling method of claim 44 does not preclude the additional functionality of providing a scaled reference signal external to the recited operating range in

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addition to the recited scaling of claim 44 in the allowed form.

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Respectfully submitted,

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